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PTO/SB/08A (08-00)

<b>Substitute for form 1449A/PTO</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		<b>Complete if Known</b>	
		Application Number	09/844,672
		Filing Date	April 30, 2001
		First Named Inventor	THEKKATH
		Group Art Unit	2193-2191
Examiner Name	Khatri, Anil		
Sheet	1 of 1	Attorney Docket Number	MTEC-005/00US

OTHER - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
u	D1.	F. Chow et al., "Engineering a RISC Compiler System," <i>IEEE COMP-CON</i> , March 1986, pp. 132-137.	
5	D2.	M.D. Smith, "Tracing with Pixie," <i>Technical Report CSL-TR-91-497</i> , Stanford University, Computer Systems Laboratory, November 1991, pp. 1-29.	
✓	D3.	ATOM Reference Manual, <i>Digital Equipment Corporation</i> , Massachusetts, December 1993, pp. 1-32.	
u	D4.	A. Srivastava et al., "ATOM: A System for Building Customized Program Analysis Tools," <i>WRL, Research Report 94/2</i> , Digital Equipment Corporation, Massachusetts, March 1994, pp. 1-23.	
✓	D5.	ATOM User Manual, <i>Digital Equipment Corporation</i> , March 1994, pp. 1-28.	
u	D6.	B. Cmelik et al., "Shade: A Fast Instruction-Set Simulator for Execution Profiling," <i>Proceedings of the 1994 ACM SIGMETRICS Conference</i> , SIGMETRICS, California, May 1994, pp. 128-137.	
u	D7.	MIPS Technologies; "MIPS64 5K™ Processor Core Integrator's Guide;" [Document No. MD00056]; <i>MIPS Technologies, Inc.</i> ; Rev. 1.2; August 11, 2000; pp. I-II, 1-82.	
u	D8.	MIPS Technologies; "MIPS64™ 5K™ Processor Core Family Integrator's Guide;" [Document No. MD00106]; ©1999-2001 <i>MIPS Technologies, Inc.</i> ; Rev. 02.00; January 15, 2001; pp. I-VIII, 1-75.	
u	D9.	MIPS Technologies; "Core Processor Interface Specification;" [Document No. MD00068]; <i>MIPS Technologies, Inc.</i> ; Rev. 1.11; March 30, 2001; pp. 1-26.	

Examiner Signature		Date Considered	2/1/04
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Unique citation designation number.

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PTO/SB/08A (08-00)

<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		<b>Complete if Known</b>	
		Application Number	09/844,672
		Filing Date	April 30, 2001
		First Named Inventor	THEKKAT
		Group Art Unit	2124-191
Examiner Name	Khatri, Anil		
Sheet	1 of 1	Attorney Docket Number	MTEC-005/00US

FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	T <sup>4</sup>
		Office <sup>1</sup>	Number <sup>2</sup>	Kind Code <sup>3</sup> (if known)			

OTHER - NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>	
u	D1	Susan J. EGGERS, et al., "Techniques for Efficient Inline Tracing on a Shared-Memory Multiprocessor," University of Washington, 1990 ACM, pp. 37-47.		
✓	D2	Richard A. UHLIG, et al., "Trace-Driven Memory Simulation: A Survey," Intel Microcomputer Research Lab; University of Michigan, <i>ACM Computing Surveys</i> , Vol. 29, No. 2, June 1997, pp. 128-170.		
✓	D3	Eric ROTENBERG, et al., "Trace Processors," University of Wisconsin, 1997 <i>IEEE Service Center</i> , 12 pp.		
✓	D4	E.N. ELNOZAHY, "Address Trace Compression Through Loop Detection and Reduction," ©1999 ACM 1-58113-083, pp. 214, 215.		
✓	D5	Darren JONES, "Opal Coprocessor Interface," <i>MIP Proprietary/Confidential</i> , June 4, 1999, pp. 1-18.		
✓	D6	Darren JONES, <i>MIPSS64™ SKC™ Processor Cores User's Manual</i> , Rev. 1.0, July 4, 1999, pp. 6-1 to 6-26.		
✓	D7	<i>MIPSS64™ SKC™ Processor Cores User's Manual</i> , Rev. 1.0.1., 1999, pp. 247-276.		

<b>Examiner Signature</b>		<b>Date Considered</b>	2/1/06
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<sup>2</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

<sup>3</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible.

<sup>4</sup> Applicant is to place a check mark here if English language Translation is attached.

<sup>1</sup> Unique citation designation number.

<sup>2</sup> Applicant is to place a check mark here if English language Translation attached.



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Substitute for form 1449A/PTO

# **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet 1 of 2

## **Complete if Known**

Application Number	09/844,672
Filing Date	April 30, 2001
First Named Inventor	THEKKATH
Group Art Unit	212- 2193
Examiner Name	Gross, Kenneth A. 1Chen
Attorney Docket Number	MTEC-005/00US

## **U.S. PATENT DOCUMENTS**

Examiner Initials <sup>a</sup>	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY
		Number	Kind Code <sup>2</sup> (if known)		
	P1	3,473,154		Couleur et al.	10/14/1969
	P2	3,585,599		Hitt	06/15/1971
	P3	3,681,534		Burian et al.	08/01/1972
	P4	3,702,989		Provenzano, Jr. et al.	11/14/1972
	P5	3,704,363		Salmasny et al.	11/28/1972
	P6	3,707,725		Dellheim	12/26/1972
	P7	3,771,131		Ling	11/06/1973
	P8	3,794,831		Frankeny et al.	02/26/1974
	P9	3,805,038		Buedel et al.	04/16/1974
	P10	3,906,454		Martin	09/16/1975
	P11	4,205,370		Hirle	05/27/1980
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	P17	4,539,682		Herman et al.	09/03/1985
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	P25	5,150,470		Hicks et al.	09/22/1992
	P26	5,274,811		Borg et al.	12/28/1993
	P27	5,289,587		Razban	02/22/1994
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	P29	5,471,594		Stone	11/28/1995
	P30	5,533,193		Roscoe	07/02/1996
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	P32	5,621,886		Alpert et al.	04/15/1997
	P33	5,689,636		Kleber et al.	11/18/1997
	P34	5,689,694		Funyu	11/18/1997
	P35	5,715,435		Bai	02/03/1998
	P36	5,751,942		Christensen et al.	05/12/1998
	P37	5,812,868		Moyer et al.	09/22/1998
	P38	5,832,515		Ledain et al.	11/03/1998
	P39	5,848,264		Baird et al.	12/08/1998
	P40	5,878,208		Levine et al.	03/02/1999
	P41	5,946,486		Pekowski	08/31/1999
	P42	6,106,573		Mahalingaiah et al.	08/22/2000
	P43	6,256,777		Ackerman	07/03/2001
	P44	6,282,701		Wygodny et al.	08/28/2001

<sup>1</sup> Unique citation designation number.

<sup>2</sup> See attached Kinds of U.S. Patent Documents.

<input checked="" type="checkbox"/>	P45	6,314,530		Mann	11/06/2001
<input checked="" type="checkbox"/>	P46	6,338,159		Alexander	01/08/2002
<input checked="" type="checkbox"/>	P47	6,353,924		Ayers et al.	03/05/2002
<input checked="" type="checkbox"/>	P48	6,457,144		Eberhard	09/24/2002
<input checked="" type="checkbox"/>	P49	6,467,083		Yamashita	10/15/2002
<input checked="" type="checkbox"/>	P50	6,487,715		Chamdani et al.	11/26/2002
<input checked="" type="checkbox"/>	P51	6,530,076		Ryan et al.	03/04/2003
<input checked="" type="checkbox"/>	P52	6,658,649		Bates et al.	12/02/2003
<input checked="" type="checkbox"/>	P53	6,684,348		Edwards et al.	01/27/2004
<input checked="" type="checkbox"/>	P54	2002/0046393		Leino et al.	04/18/2002
<input checked="" type="checkbox"/>	P55	2002/0147965		Swaino et al.	10/10/2002

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY
		Office <sup>2</sup>	Number <sup>3</sup>	Kind Code <sup>3</sup> (if known)		
<input checked="" type="checkbox"/>	F1	GB	2 329 048		ARM Limited	10/03/1999
<input checked="" type="checkbox"/>	F2	GB	2 329 049		ARM Limited	10/03/1999

Examiner Signature		Date Considered	3/28/15
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Substitute for form 1449A/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

*(use as many sheets as necessary)*

Sheet	1	of	2
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**Complete if Known**

Application Number	09/844,672
Filing Date	April 30, 2001
First Named Inventor	THEKKATH
Group Art Unit	2183
Examiner Name	Unassigned
Attorney Docket Number	MTEC-005/00US (0122.00US)

## U.S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

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**Examiner  
Signature**

**Date  
Considered**

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Substitute for form 1449B/PTO		<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		Application Number	09/844,672
		Filing Date	April 30, 2001
		First Named Inventor	THEKKATH
		Group Art Unit	3483 2193
		Examiner Name	Unassigned Khatri
Sheet 2 of 2	Attorney Docket Number	MTEC-005/00US (0122.00US)	

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
✓	C1	MIPS64 5Kc™ Processor Core Datasheet, Revision 1.7.4, pp1-40, December 14, 1999	
✓	C2	MIPS64 5Kc™ Processor Core Datasheet, Revision 1.7.5, pp1-40, August 11, 2000	
✓	C3	MIPS64 5Kc™ Processor Core Datasheet, Revision 2.0, pp1-44, August 28, 2000	
✓	C4	MIPS64™ 5Kc™ Processor Core Datasheet, Revision 00.11, pp1-44, March 30, 2001	
✓	C5	MIPS64 5Kc™ Processor Core Software User's Manual, Revision 2.2, pp1-580, August 11, 2000	

Examiner Signature	<i>[Signature]</i>	Date Considered	2/18/05
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PTO/SB/08A (09-00)

<b>Substitute for form 1449B/PTO</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		<b>Complete if Known</b>			
		Application Number	09/844,673		
		Filing Date	April 30, 2001		
		First Named Inventor	THEKKATH		
		Group Art Unit	2122 2143		
Sheet	1	of	1	Examiner Name	Cross, Kenneth A. Khatri
				Attorney Docket Number	MTEC-005/00US

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
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K	1	Embedded Trace Macrocell Specification, Rev. 0/0a, ARM IHI 0014C, ARM Ltd. (1999)	

Examiner Signature		Date Considered	8/28/05
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